

# LCD Module Specification

# ITEM NO.: LCM1610SLY

## **Table of Contents**

1. COVE & CONTENTS	1
2. RECORD OF REVISION	2
3. GENERAL SPECIFICATION	3
4. ABSOLUTE MAXIMUM RATINGS	4
5. ELECTRICAL CHARACTERISTICS	.5
6. ELECTRO-OPTICAL CHARACTERISTIC	.5
7. TIMING CHARACTERISTICS	8
8. PIN CONNECTIONS	10
9. POWER SUPPLY	10
10. BLOCK DIAGRAM1	1
11. QUALITY ASSURANCE1	8
12. LOT NUMBERING SYSTEM	22
13. LCM NUMBERING SYSTEM	22
14. PRECAUTION IN USE LCM2	23
15. OUTLINE DRAWING	24

Approved by:	Checked by:	Checked by:		Drawn by:	
	Pro.l	.Div.	R&D.Div.		
Final Revision:	Sheet Code:	Issued Date:	1998/08/19	Total Page:	24

# 2. RECORD OF REVISION

Rev	Date	Item	Page	Comment

# 3. GENERAL SPECIFICATION

Display Format :	16characters (W) $\times$ 1lines (H)
Character Size :	3.07 (W) × 6.56 (H) mm
View Area :	65 (W) × 14 (H) mm
General Dimensions :	80 (W) $\times$ 36 (H) $\times$ 15 (T) mm Max.
Weight :	36 g max.
LCD Type :	VSTN Gray STN Yellow FSTN
Polarizer mode :	Reflective
	Transmissive Negative
View Angle :	V 6 O' clock 12 O' clock Others
Backlight :	VLED EL CCFL
Backlight Color :	V Yellow green Amber Blue Green
	White Others
Controller / Driver :	KS0066
Temperature Range :	V Normal Wide Temperature Operating 0 to 50°C Operating -20 to 70°C Storage -20 to 70°C Storage -30 to 80°C

## 4. ABSOLUTE MAXIMUM RATINGS

### 4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

			$v_{SS} = 0$	0V, 1a = 23
ltem	Symbol	Min.	Max.	Unit
Supply Voltage (Logic)	Vdd-Vss	0	7	V
Supply Voltage (LCD Driver)	Vdd-Vee	1.5	13.5	V
Input Voltage	Vi	Vss	Vdd	V
Operating Temperature	Тор	0	50	°C
Storage Temperature	Tstg	-20	70	°C

Vss= 0V, Ta =  $25^{\circ}C$ 

#### 4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ltem	Operating		Sto	orage	Commont	
nem	(Min.)	Max.)	(Min.)	(Max.)	Comment	
Ambient Temp	0	50	-20 70		Note (1)	
Humidity	Note (2)		Note (2) Note(2)		Without Condensation	
Vibration		4.9M/S <sup>2</sup>		19.6M/S <sup>2</sup>	XYZ Direction	
Shock		29.4M/S <sup>2</sup>		490M/S <sup>2</sup>	XYZ Direction	

Note(1) Ta =  $0^{\circ}C$ : 50Hr Max.

Note(2) Ta  $\leq$  40°C : 90% RH Max.

Ta  $\geq 40^{\circ}$ C : Absolute humidity must be lower than the humidity of 90% RH at 40°C.

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	
Supply Voltage (Logic)	VDD-VSS		4.75	5.0	5.25	V	
Supply Voltage		0°C	4.3	4.5	4.7		
	Vdd-Vee	25°C	4.1	4.3	4.5	V	
		50°C	3.6	3.8	4.0		
	Vін		0.7*Vdd		Vdd	V	
input voltage	VIL		Vss		0.3*Vdd	V	
Logic Supply Current	IDD	VDD-VSS=5V		2.0		mA	

# 5. ELECTRICAL CHARACTERISTICS

# 6. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	Symbol	Condition	Min.	Тур.	Max.	Unit	Ref.
D. T.	т.,	0°C		1100	1800	100.0	
Rise Time	Iſ	25°C		420	670	ms	Noto (1)
Fall Time	Τf	0°C		210	340	me	
	11	25°C		100	300	1115	
Contrast	CR	25°C					Note (3)
View Angle	θ1~θ2	25°C &		80			Note (2)
	Ø1, Ø2	CR≥3		30			Note (2)
Frame Frequency	Ff	25°C		64		Hz	

Note (1) & (2) : See next page

Note (3) : Contrast ration is defined under the following condition:

CR= <u>Brightness of non-selected condition</u> Brightness of selected condition

- ( a ). Temperature ----- 25°C
- (b). Frame frequency ---- 64Hz
- (c). Viewing angle -----  $\theta = 0^{\circ}$ ,  $\emptyset = 0^{\circ}$
- (d). Operating voltage --- 4.3V

Note (1) Response time is measured as the shortest period of time possible between the change in state of an LCD segment as demonstrated below:



Condition:

- (a). Temperature -----25°C
- (b). Frame frequency ----- 64Hz
- (c). View Angle -----  $\theta = 0^{\circ}, \emptyset = 0^{\circ}$
- (d). Operating voltage ------ 4.3V









#### 6.1 LED ELECTRO-OPTICAL CHARACTERISTIC

 $Ta = 25^{\circ}C$ 

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF = 130mA Yellow Green		4.2	4.6	V
Luminous Intensity	Iv	IF = 130mA Yellow Green	120	190		cd/m <sup>2</sup>
Peak Emission	λP	IF = 130mA Yellow Green		570		nm
Spectrum Radiation	Δλ	IF = 130mA Yellow Green		30		nm
Reverse Current	IR	VR = 8V Yellow Green			0.2	mA

Note : Measured at the bard LED backlight unit.

#### 6.2 LED MAXIMUM OPERATING RANGE

Item	Symbol	Yellow Green	Unit
Power Dissipation	Pad	0.8	W
Forward Current	laf	180	mA
Reverse Voltage	VR	8	V

# 6.2.1 LED ARRAY BLOCK DIAGRAM



#### 6.2.2 LED POWER SOURCE

	Option	Power source	Jumper setting	
	A	VDD/VSS	J1,J3,R9	
LED	В	15K/16A	J2,J5,J7	
	С	A/K	NONE	
	Nil	15A/16K	J2,J4,J6	
	BZ	L GND	J8	
GND	FR	M GND	J9	

# 7. TIMING CHARACTERISTICS

### 7.1 WRITE TIMING

Item	Symbol	Condition	Min.	Max.	Unit
Enable cycle time	tcycE		500		
Enable pulse width (high level)	PWEH		220		
Enable rise/fall time	tEr, tEf			25	
Address set-up time (RS, R/W,to E)	tAS	Vdd = 5V	40		ns
Address hold time	tAH		10		
Data set-up time	tDSW	-	60		
Data hold time	tH		10		



## 7.2 READ TIMING

Item	Symbol	Condition	Min.	Max.	Unit
Enable cycle time	tcycE		500		
Enable pulse width(high level)	PWEH		220		
Enable rise/fall time	tEr, tEf			25	
Address set-up time (RS, R/W,to E)	tAS	Vdd = 5V	40		ns
Address hold time	tAH		10		
Data set-up time	tddr			120	
Data hold time	tDHR		5		



# 8. PIN CONNECTIONS

No.	Symbol	Function
1	VSS	Ground, 0V
2	VDD	Logic power supply, +5V
3	VEE	Voltage for LCD drive
4	RS	Data / Instruction register select
5	R/W	Read / Write
6	E	Enable signal, start data read/write
7	DB0	
8	DB1	
9	DB2	
10	DB3	Data Bus Line
11	DB4	
12	DB5	
13	DB6	
14	DB7	
15	LED A	LED Anode, power supply +
16	LED K	LED Cathode, ground 0V

# 9. POWER SUPPLY



VR = 10K

# **10. BLOCK DIAGRAM**



T		CODE									DECONDICAL	Executed
Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	DESCRIPTION	focs=250KHz
Clear Display	0	0	0	0	0	0	0	0	0	1	Clear all display and returns the cursor to the home position (Address 0)	1.64mS
Cursor At Home	0	0	0	0	0	0	0	0	1	*	Returns the cursor to the home position (Address 0). Also returns the display being shifted to the original position DDRAM contents remain unchanged	1.64mS
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets the cursor move direction and specifies or not to shift the display. These operations are performed during data write and read.	40µS
Display On/Off Control	0	0	0	0	0	0	1	D	С	В	Sets the ON/OFF of all display (D) cursor ON/OFF (C), and blink of cursor position character (B)	40µS
Cursor/Display Shift	0	0	0	0	0	1	S/C	R/L	*	*	Moves the cursor and shifts the display without changing DDRAM contents	40µS
Function Set	0	0	0	0	1	DL	N	F	*	*	Sets interface data length (DL), number of display lines(N) and character font (F).	40µS
CGRAM Address Set	0	0	0	1	A <sub>CG</sub>	ŕ					Sets the CGRAM, data is sent and received after this setting.	40µS
DDRAM Address Set	0	0	1	$A_{DD}$							Sets the CGRAM, data is sent and received after this setting.	40µS
Busy Flag/ Address Read	0	1	BF	AC	2					Reads Busy flag (FB) indicating internal operation is being performed and reads address counter contents.	0µS	
CGRAM/DDRAM Data Write	1	0	W <sub>RI</sub>	TE D	D <sub>ATA</sub>			Writes data into DDRAM or CGRAM	40µS			
CGRAM/DDRAM Data Read	1	1	R <sub>EAI</sub>	$_{\rm D}{\rm D}_{\rm A'}$	D <sub>ATA</sub>					Reads data into DDRAM or CGRAM	40µS	

### 10.1 INSTRUCTIONS

	Code		Description	Executed Time (max.)
I/D=1: Increment I/D=0: Decrement S=1: With display shift S/C=1: Display shift	DL = 0:4-bit 1/16 duty 1/8 duty, 1/11 duty F= 1: 5x10 dots	DDRAM: CGRAM: ACG: ADD:	Display Data RAM Character Generator RAM CGRAM Address DDRAM Address	Fcp or focs = 250kHz However, when frequency changes, execution time also changes
S/C=0: Cursor movement R/L=1: Shift to the right R/L=0: Shift to the left DL=1 : 8-bit	F=0: 5x7 dots BF=1: Internal Operation is being performed BF=0: Instruction acceptable	AC: *:	Corresponds to cursor address Address Counter, used for both DDRAM and CGRAM Invalid	Example If fcp or focs is 270kHz 40µS x <del>250 =</del> 37µS 270

# 10.2 8-Bit Operation, 8-Digit 2-Line Display Example

Step					Instru							
No	RS         R/W         DB7         DB6         DB5         DB4         DB3         DB2         DB1         DB0										Display	Operation
1	Pov Inte	ver suj ernal r	pply o eset c	on (the	e HD4 t)	4780	the		Initialized. No display.			
2	Fur	nction	set				Sets to 8-bit operation and					
	0	0	0	0	1	1	1	0	*	*		selects 2-line display and 5-8 dot character font.
3	Dis	play o	n/off o	contro	bl							Turns on display and cursor.
	0	0	0	0	0	0	1	1	1	0		All display is in space mode because of initialization.
4	Ent	rv mo	de set									Sets mode to increment the
_	0	 	0	0	0	0	0	1	1	0		address by one and to shift the
	U	U	U	U	U	U	U	T	1	U		cursor to the right at the time of write to the DD/CGRAM.
												Display is not shifted.
5	Wr	ite dat	ta to C	CGRA	M/DI	DRAN	<b>I</b>				Н	Writes H. DDRAM has already
	1	0	0	1	0	0	1	0	0	0		been selected by initialization
	-	v	v	-	Ū	Ū	-	Ū	Ū	v		on. The cursor is incremented
												by one and shifted to the right
6						× × ×					× × ×	
7	Wr	ite dat	ta to C	CGRA	M/DI	DRAN	ſ				HITACHI	Writes I.
	1	0	0	1	0	0	1	0	0	1		
8	Set	DDRA	AM ad	ldress	5						HITACHI	Sets DDRAM address so that t
	0	0	1	1	0	0	0	0	0	0	-	The cursor is positioned at the Head of the second lime
9	Wr	ite dat	- a to (		M/DI	DRAN	<u></u>	-	-	-		Writes M.
	1			1	<u> </u>	Λ	1	1	Δ	1	HITACHI M_	
10	1	U	U	1	U	• •	1	L	U	1	 ×	
10						× × ×					× × ×	
11	Wr	ite dat	ta to (	CGRA	M/D	DRAN	Λ				HITACHI	Writes O.
	1	0	0	1	0	0	1	1	1	1	MICROCO_	
12	Ent	ry mo	de set								HITACHI	Sets mode to shift display at
	0	0	0	0	0	0	0	1	1	1	MICROCO_	the time of write.
13	Wr	ite dat	a to C	CGRA	M/DI	DRAN	Л				ITACIH	Writes M. Display is shifted to
	1	0	0	1	0	0	- 1	1	0	1	ICROCOM_	the left. The first and second
	1	U	U	T	U	U	T	Ŧ	U	1		lines both shift at the same time.
14						× ×					× ×	
						× ×					× ×	
15	Ret	urn ha	ome			•						Returns both display and
		N	0	A	A	A	Λ	A	1	0	MICROCOM	cursor to the original position
	U	U	U	U	U	U	U	U	T	U		(address 0).

### 10.3 Interfacing to the MPU

The HD44780U can send data in either two 4-bit operations, thus allowing interfacing with 4- or 8-bit MPUs.

• For 4-bit interface data, only four bus lines (DB4 to DB7) are used for transfer. Bus lines DB0 to FB3 are disabled. The data transfer between the HD44780U and the MPU is completed after the 4-bit data has been transferred twice. As for the order of data transfer, the four high order bits (for 8-bit operation,DB4 to DB7) are transferred before the four low order bits (for 8-bit operation, DB0 to DB3).

The busy flag must be checked (one instruction) after the 4-bit data has been transferred twice. Two more 4-bit operations then transfer the busy flag and address counter data.



4-Bit Transfer Example









#### 10.5 CGRAM

Relationship between CGRAM Addresses, Character Codes (DDRAM) and Patterns (CGRAM Data)

Character Codes (DDRAM data)	CGRAM	I Address	Character Patterns (CGRAM data)	
7 6 5 4 3 2 1 0	5 4 3	2 1 0	7 6 5 4 3 2 1 0	
High Low	High	Low	High Low	
0000*000	000	$\begin{array}{ccccccc} 0 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 1 & 0 \\ 0 & 1 & 1 \\ 1 & 0 & 0 \\ 1 & 0 & 1 \\ 1 & 1 & 0 \end{array}$	$\begin{array}{c} * & * & * \\ \bullet & & 1 \\ \bullet & & 0 \\ \bullet & & 1 \\ \bullet & & 0 \\ \bullet & & 1 \\ \bullet & & 0 \\ \bullet & & 1 \\ \end{array}$	Character Pattern (1)
		1 1 1	* * * 0 0 0 0	} Cursor position
0000*001	0 0 1	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	$\begin{array}{c} * & * & * \\ \bullet & \bullet & * \\ \bullet & \bullet & \bullet \\ \bullet & \bullet & \bullet \\ \bullet & \bullet & \bullet \\ \bullet & \bullet &$	Character Pattern (2) } Cursor position
		0 0 0 0 0 1	* * *	
	I			ļ
0000*111	1 1 1	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	* * *	

For 5. 8 dot character patterns

- Notes: 1. Character code bits 0 to 2 correspond to CGRAM address bits 3 to 5 (3 bits: 8 types).
  - 2. CGRAM address bits 0 to 2 designate the character pattern line position. The 8th line is the cursor position and its display is formed by a logical OR with the cursor. Maintain the 8th line data, corresponding to the cursor display position, at 0 as the cursor display.

If the 8th line data is 1, 1 bits will light up the 8th line regardless of the cursor presence.

- **3.** Character pattern row positions correspond to CGRAM data bits 0 to 4 (bit 4 being at the left).
- 4. As shown Table 5, CGRAM character patterns are selected when character code bits 4 to 7 are all 0. However, since character code bit 3 has no effect, the R display example above can be selected by either character code 00H or 08H.
- 5. 1 for CGRAM data corresponds to display selection and 0 to non-selection.
- \* Indicates no effect.

	0000	0001	0010	0011	0100	0101	Q1 1Q	0111	1000	1001	1010	1011	1100	1101	11 10	1111
xxx0000	83 8004 (1)			0	ð	P		P				-	2	Ę	Q,	p
xxxx0001	(2)			1	A	Q	а	9				7	Ŧ	4	ä	q
xxxx0010	(3)		Π	2	В	R	b	r			F	1	Ņ	X	ß	8
xxxx0011	(4)		#	3	С	5	C.	S			L	7	Ŧ	Ŧ	ε.	67
xxxx0100	(5)		\$	4	D	T	d	t			۰.	Ι	ŀ	Þ	Ч	Ω
xxxx0101	(6)		7	5	E	U	e	u				7	Ŧ	l	G	ü
xxxx0110	7)		8	6	F	Ų	f	V			7	'n	-	Ξ	ρ	Σ
xxxx0111	(8)		7	7	G	W	9	W			7	Ŧ	Z	7	q	π
xxxx 1000	(1)		C	8	Η	X	h	X			4	2	7	Ņ	<u>,</u> г	$\overline{\mathbf{X}}$
xxxx 100 1	(2)		Σ	9	I	Y	i	ч			÷	ን	Ţ	լի	-1	Ч
xxxx 1010	(3)		*		J	Ζ	j	Ζ			T		Ù	$\boldsymbol{\nu}$	j	Ŧ
xxxx1011	(4)		╋	;	K		k	{			7	Ħ	F		x	Б
xxxxx1100	(5)		,	K		¥	1				ħ	IJ	フ	7	¢	Ħ
xxxx1101	(6)		-	=	М		M	}			ユ	Z	ኅ	2	Ł	÷
xxxxx1110	7)			>	Ν	^	n	→			Ξ	t	<b>.</b>	s,	ñ	
xxxx1111	(8)		/	?	0		O	÷			••	У	7		Ö	

10.6 Correspondence between Character Codes and Character Patterns (ROM Code:A00)

Note: The user can specify any pattern for character-generator RAM.

## 11. QUALITY ASSURANCE

### 11.1 Test Condition

- 11.1.1 Temperature and Humidity(Ambient Temperature) Temperature :  $20 \pm C$ Humidity :  $65 \pm 5\%$
- 11.1.2 Operation

Unless specified otherwise, test will be conducted under function state.

11.1.3 Container

Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

11.1.4 Test Frequency

In case of related to deterioration such as shock test. It will be conducted only once.

11.1	1.5	Test	Method
		1000	mounda

No.	Parameter	Conditions	Regulations
1	High Temperature Operating	50 ± 2 °C	Note 3
2	Low Temperature Operating	0 ± 2 °C	Note 3
3	High Temperature Storage	70 ± 2 °C	Note 3
4	Low Temperature Storage	-20 ± 2 °C	Note 3
5	Vibration Test (Non-operation state)	Total fixed amplitude : 1.5mm Vibration Frequency : 10 ~ 55Hz One cycle 60 seconds to 3 directions of X.Y.Z. for each 15 minutes	Note 3
6	Damp Proof Test (Non-operation state)	40°C ± 2°C, 90~95%RH, 96h	Note 1,2
7	Shock Test (Non-operation state)	To be measured after dropping from 60cm high once concrete surface in packing state	Note 3

Note 1: Returned under normal temperature and humidity for 4 hrs.

Note 2: No dew condensation to be observed.

Note 3: No change on display and in operation under the test condition

# 11.2 Inspection condition

#### 11.2.1 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light. The distance between the eyes and the sample shall be more than 30 cm. All directions for inspecting the sample should be within  $45^{\circ}$  against perpendicular line.



11.2.2 Definition of applicable Zones



A : Viewing area

B : Active area

NO.	Parameter	Criteria						
1	Black and white Spots Foreign Substances	Round ShapeZoneAcceptable numberClass Of DefectsAcceptable 						
2	Air Bubbles (between glass & polarizer)	X : LengthY : Width* : DisregardTotal defects should not exceed 5 $\overline{\text{Zone}}$ AcceptableClassAcceptableDimensionABCDefectsD $\leq 0.3$ * * *Minor2.5 $0.3 < D \leq 0.4$ 3 * *Minor2.5 $0.4 < D \leq 0.6$ 23 *Minor* : DisregardTotal defects shall not excess 3.						



## 12. LOT NUMBERING SYSTEM



# 13. LCM NUMBERING SYSTEM



## 14. PRECAUTION FOR USING LCM

#### 1. LIQUID CRYSTAL DISPLAY (LCD)

LCD is made up of glass, organic sealant, organic fluid, and polymer based polarizers. The following precautions should be taken when handing,

(1). Keep the temperature within range of use and storage. Excessive temperature and humidity could cause

polarization degredation, polarizer peel off or bubble. (2). Do not contact the exposed polarizers with anything harder than an HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzin.

(3). Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or color fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.

(4). Glass can be easily chipped or cracked from rough handling, especially at corners and edges.

(5). Do not drive LCD with DC voltage.

2. Liquid Crystal Display Modules

2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted. (1). Do not tamper in any way with the tabs on the metal

(1) Do not multiply in any way with the table of the frame.(2). Do not modify the PCB by drilling extra holes,

changing its outline, moving its components or modifying its pattern.

(3). Do not touch the elastomer connector, especially insert an backlight panel (for example, EL).

(4). When mounting a LCM make sure that the PCB is not under any stress such as bending or twisting . Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.

(5). Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

#### 2.2. Static Electricity

LCM contains CMOS LSI's and the same precaution for such devices should apply, namely

(1). The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.

(2). The modules should be kept in antistatic bags or other containers resistant to static for storage.

(3). Only properly grounded soldering irons should be used.

(4). If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

(5) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.(6). Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.

2.3 Soldering

(1). Solder only to the I/O terminals.

(2). Use only soldering irons with proper grounding and no leakage.

(3). Soldering temperature :  $280^{\circ}C \pm 10^{\circ}C$ 

(4). Soldering time: 3 to 4 sec.

(5). Use eutectic solder with resin flux fill.

(6). If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed after wards.

#### 2.4 Operation

(1). The viewing angle can be adjusted by varying the LCD driving voltage V0.

(2). Driving voltage should be kept within specified range; excess voltage shortens display life.

(3). Response time increases with decrease in temperature.

(4). Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".

(5). Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".

#### 2.5 Storage

If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all the time.

#### 2.6 Limited Warranty

Unless otherwise agreed between LCD Micro. and customer, LCD Micro. will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with LCD Micro. acceptance standards, for a period on one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of LCD Micro. is limited to repair and/or replacement on the terms set forth above. LCD Micro. will not responsible for any subsequent or consequential events.





.

 $\Box$ 



1.6

6.56

Ť

A BLOCK

]	PIN	FUNC	OIT	٧S
1.	VSS		9.	DB2
2.	VDD	)	10.	DB3
3.	VEE	1	11.	DB4
4.	$\mathbb{RS}$		12.	DB5
5.	R/V	V	13.	DB6
6.	Е		14.	DB7
7.	DBC	)	15.	LEDA
8.	DB1		16.	LEDK

Page: 24 / 24